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ANALYSIS AND IMPROVEMENT OF VIRTEX-4 BLOCK RAM BUILT-IN SELF-TEST



LAP Lambert Acad. Publ. Nov 2010, 2010. Taschenbuch. Book Condition: Neu. 220x151x14 mm. Neuware - A reliable method for testing embedded memories within Virtex-4 and Virtex-5 Field-Programmable Gate Arrays (FPGAs) is needed by the current FPGA community. A method for testing the Virtex-4 embedded Block Random Access Memories (RAMs) using built-In Self-Test(BIST) was initially proposed by Daniel Milton in Built-In Self-Test of Configurable Memory Resources in Field-Programmable Gate-Arrays. However, this method was found to have deficiencies in practical application. Several...

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- Authored by Brooks Garrison
- Released at 2010



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